

Faculty Profile

Name : Suman lata
Designation : Assistant Professor
Qualifications : Pursuing Ph.d, M.Tech, B.Tech
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Area of Interest/Specialization: Embedded Systems, Microcontrollers
Experience : 10 years



Key Publications

1. Suman Lata et.al, “3D -DWT VLSI Architecture for Image Processing” in International conference titled “Intelligent Systems & Nanotechnology” held in ISTK Jagadhri on 26th - 27th Feb 2010, IISN-2010.
2. Suman Lata et.al, “2-D DWT Architecture for Memory & Speed Optimization using VHDL” in national conference titled “Exploring Women Potential in Engineering” held at CITC, Changa, Gujrat on 03th&04th July 2009.
3. Suman Lata et.al, “Simulation of 8×10 Gb/s WDM system based on optical amplifier at different dispersion” in International Journal of Computer, Electronics & Electrical Engineering (ISSN: 2249 - 9997) Volume 2– Issue 3.

Papers Presented in Conferences

1. Suman Lata et.al, “Design of CMOS Voltage-Controlled Ring Oscillator with Wide Tuning Range” presented in TEQIP Sponsered National Conference on contemporary Techniques & Technologies in electronic engineering, organised by DCRUST Murthal on 13-14 March, 2013.

Patent/Copyright

Nil

Sponsored Project/Consultancy:

Nil

Book Chapter/Books published:

Nil

Ph.D Supervised:

Nil

Memberships of Professional bodies:

No

Other Contributions